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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	New Application
				Filing Date	December 10, 2003
				First Named Inventor	Naoaki SUGIMURA
				Art Unit	
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	031794-9

U.S. PATENT DOCUMENTS					
Examiner Initials ¹	Cite No. ¹	U.S. Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		JP 07-79123	03/20/1995	Kawai et al.		Abstract
		JP 08-116223	05/07/1996	Chorun-Kuan et al.		Abstract
		JP 2003-69352	03/07/2003	Amachi et al.		Abstract

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁷
		2001 IEEE International Solid-State Circuits Conference, pp. 130-131, Yong-In park et al. (English)	
		2001 IEEE International Solid-State Circuits Conference, pp. 134-135, Dan Kelly et al. (English)	
		"Analog Integrated Circuit", pp. 262-264, Alen B. Grebene (English translation and its original in Japanese)	

Examiner Signature	S J MOTTOYA	Date Considered	2-8-05
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